

Lecture 2: Feedback Structures, Digital Control and Stability

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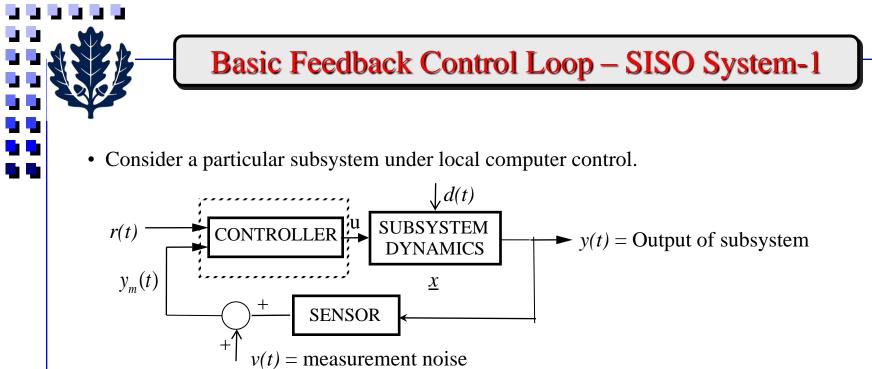
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ECE 6095/4121 Digital Control of Mechatronic Systems



Mechatronic Systems Introduction & Overview

- Feedback Control Structures for Continuous-time Systems
 - Output feedback (series compensation, Proportional-Integral-Derivative (PID), Feed forward-feedback, Internal Model Control...)
 - State variable feedback
- 2. Classification of Control Design Techniques
- 3. Digital Control Loop Structure
 - Relationships among time signals
 - Typical algorithm implementation considerations
- 4. Discrete-time System Stability
- 5. Continuous-time-vs.-Discrete-time Relationships
 - $s \rightarrow z$ plane mapping



The feedback control problem is to design the controller to have $y(t) \approx r(t) =$ reference setpoint, even in the face of external disturbances, imprecise models of dynamics, etc. Error, $e(t) \triangleq r(t) - y(t)$.

We define u(t) = control signal produced by the controller. This is the input signal to the subsystem (e.g., valve opening, fuel flow, etc.)
 If only measurements of the system output are available:

unknown $d(t): u(t) = H[r(\bullet), y_m(\bullet)];$ measured $d(t): u(t) = H[r(\bullet), d(\bullet), y_m(\bullet)]$

Output Feedback

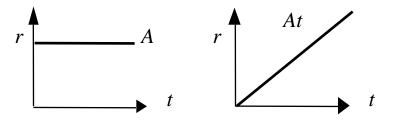


Basic Feedback Control Loop – SISO System-2

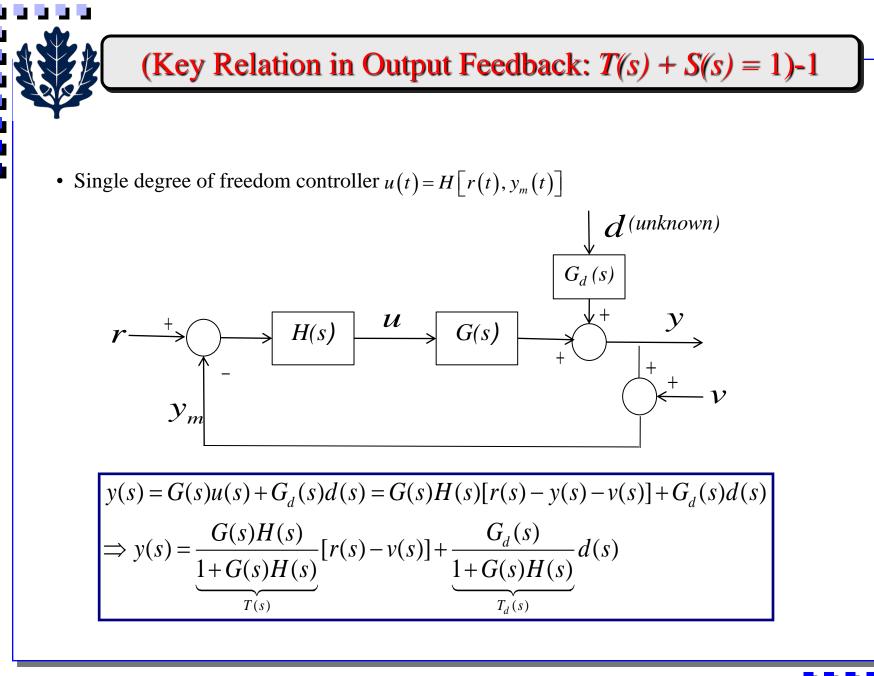
Often we have measurements of the system state \underline{x} available for feedback, in which case:

 $u(t) = H[r(\bullet), \underline{x}(\bullet)]$ <u>State Variable Feedback</u>

- When r(t) = 0, we desire $y \ge 0$. This is a **regulator** problem, where we wish to bring the system to the rest state (e.g., reduce the spin of a satellite, Linearized non-linear system around an equilibrium point, maintain speed (slip) variations near zero in an induction motor).
- When $r(t) \neq 0$, we have an **output command (servo)** problem, where we wish to reduce $e(t) \rightarrow 0$. Typically, r(t) is a step or ramp command.







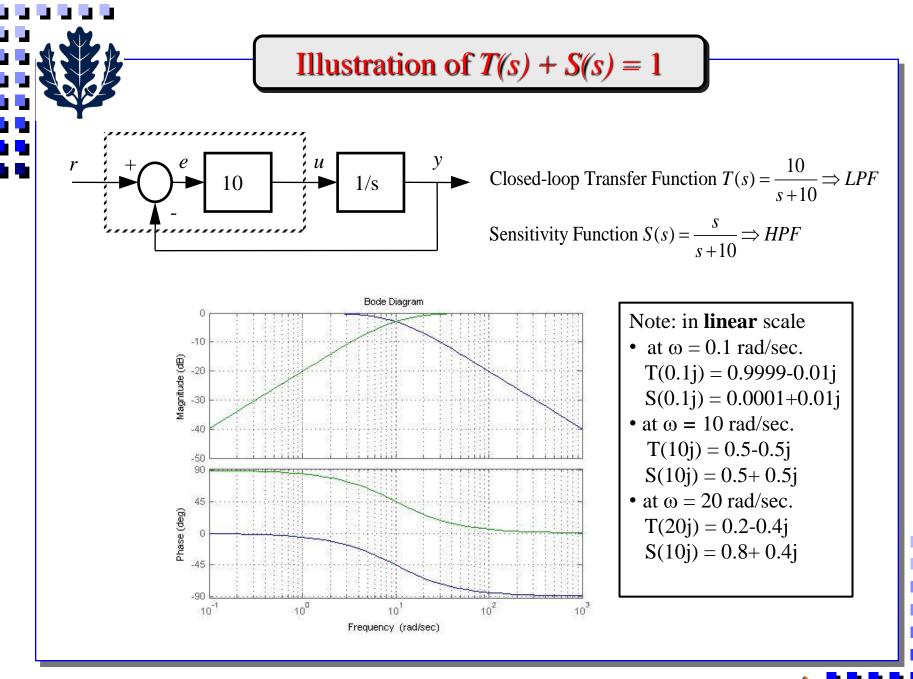
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(Key Relation in Output Feedback: T(s) + S(s) = 1)-2

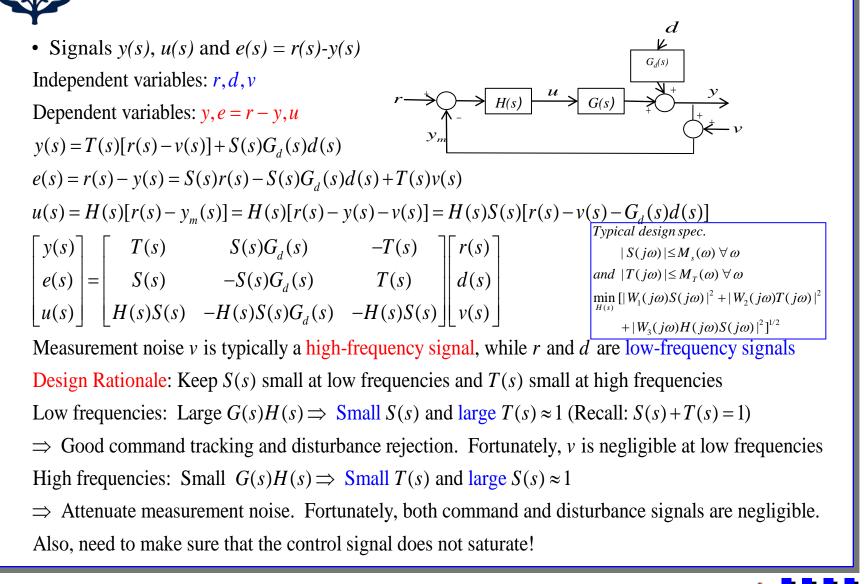
Let us look at output signal y(s), control signal u(s), and error signal e(s)

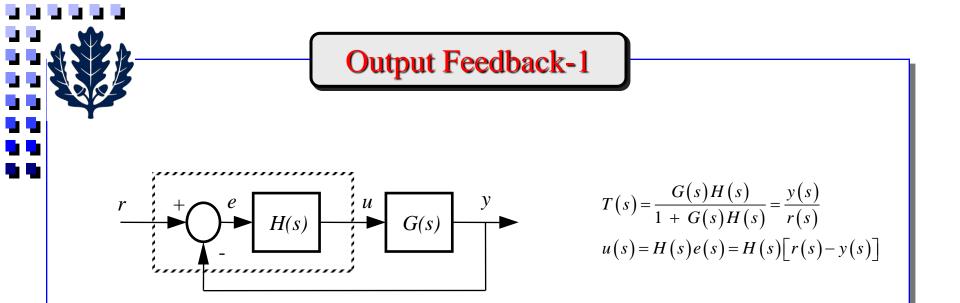




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Fundamental Tradeoffs in SISO Control Design



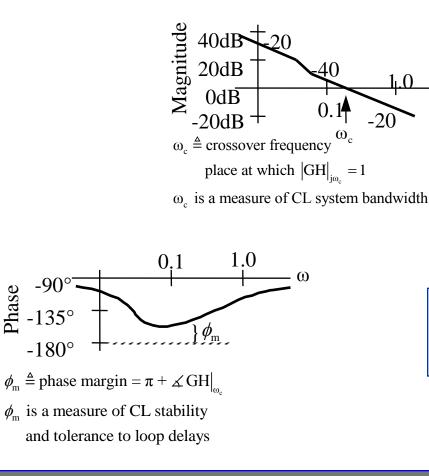


Design objective is to determine the transfer function *H*(*s*) so that *y*(*t*) → *r*(*t*) "nicely" and the closed-loop has desirable stability/transient response. Usually, *H*(*s*) has a simple form, e.g.,



Output Feedback-2

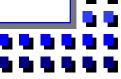
• Selection of design parameters (K, α , ω_1 , ...) via either root locus or Bode plot methods => "classical design", using properties of loop gain, *GH*.

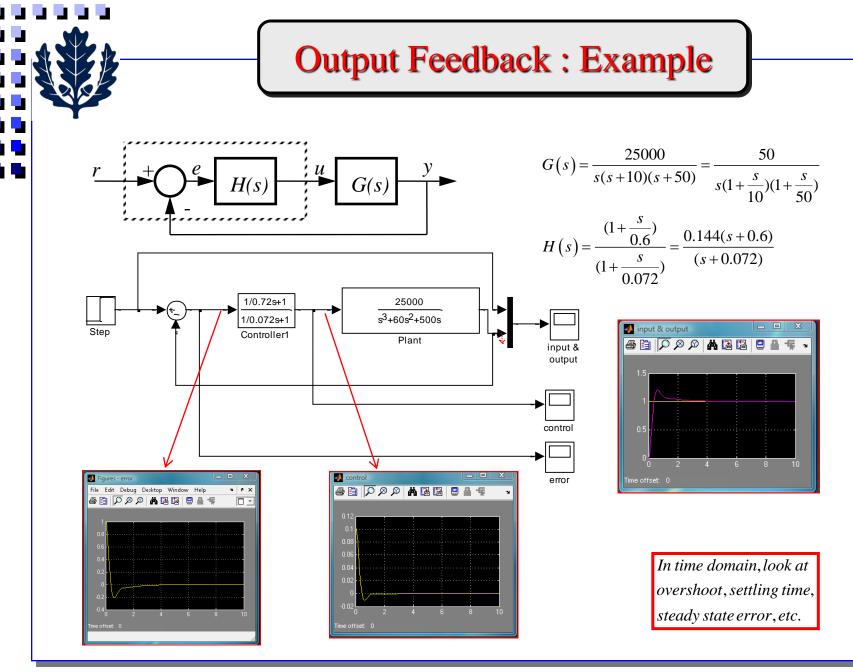


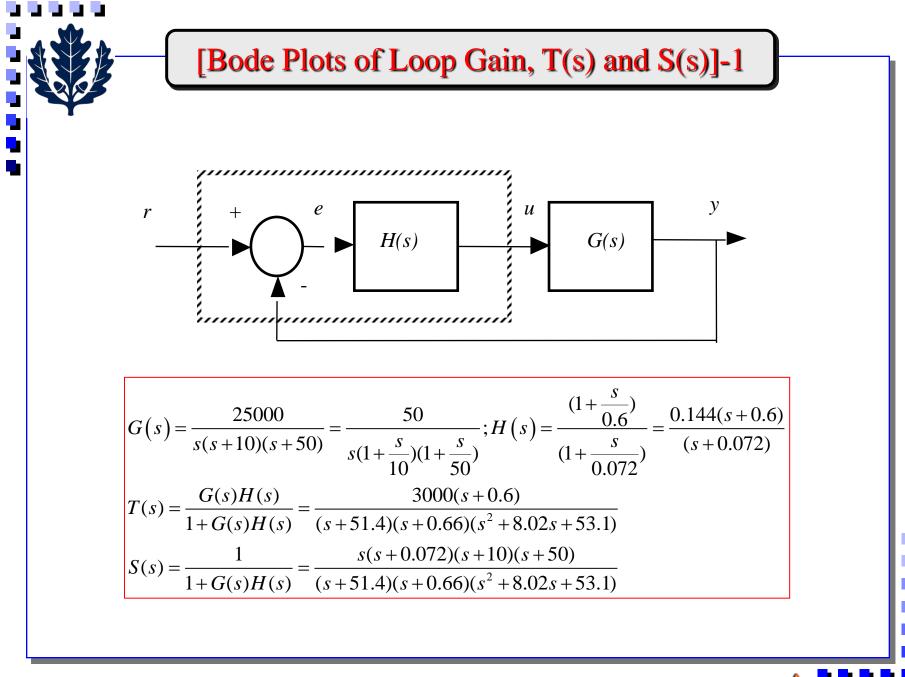
$$\begin{bmatrix} \text{Recall } \measuredangle e^{-s\tau} \Big|_{s=j\omega_{c}} = -\omega_{c}\tau \\ \Rightarrow \tau_{\max} = \phi_{m}/\omega_{c} \end{bmatrix}$$

ω

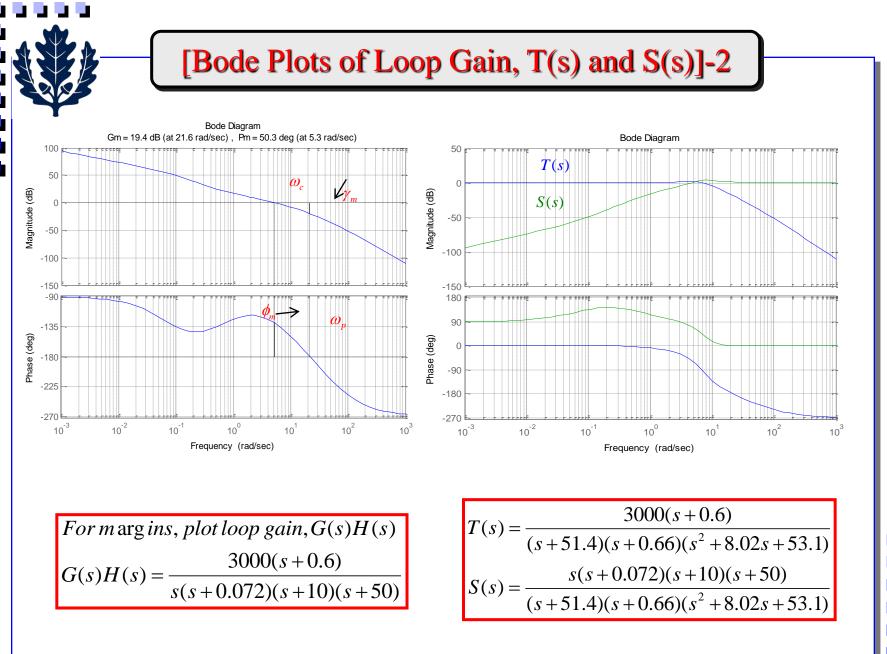
A good design will have: $\phi_{\rm m} \sim 45^{\circ}$ to 60°



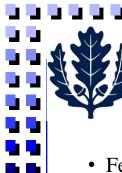




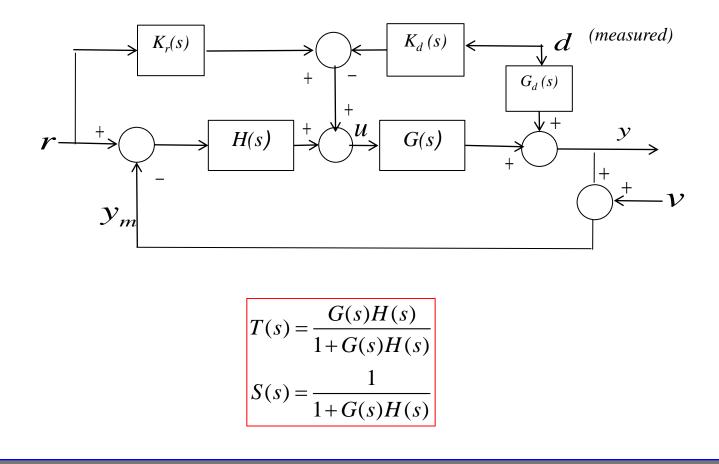
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• Feedback and Feedforward scheme (*d* is measured)



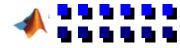


Feedforward Schemes-2

$$\begin{bmatrix} y(s) \\ e(s) \\ u(s) \end{bmatrix} = \begin{bmatrix} T(s) + S(s)G(s)K_r(s) & S(s)[G_d(s) - G(s)K_d(s)] & -T(s) \\ S(s)[1 - G(s)K_r(s)] & -S(s)[G_d(s) - G(s)K_d(s)] & T(s) \\ [H(s) + K_r(s)]S(s) & -[H(s)S(s)G_d(s) + S(s)K_d(s)] & -H(s)S(s) \end{bmatrix} \begin{bmatrix} r(s) \\ d(s) \\ v(s) \end{bmatrix}$$

- Feedforward control helps at frequencies where $|S(j\omega)| > 1$
- For command tracking, make $S(s)[1-G(s)K_r(s)]$ small $\Rightarrow K_r(s) = \frac{1}{G(s)}$
- For disturbance rejection, make $S(s)[G_d(s) G(s)K_d(s)]$ small $\Rightarrow K_d(s) = \frac{G_d(s)}{G(s)}$
- Problems: 1) What if G(s) is not stable?, 2) What if $\frac{1}{G(s)}$ is non-causal?

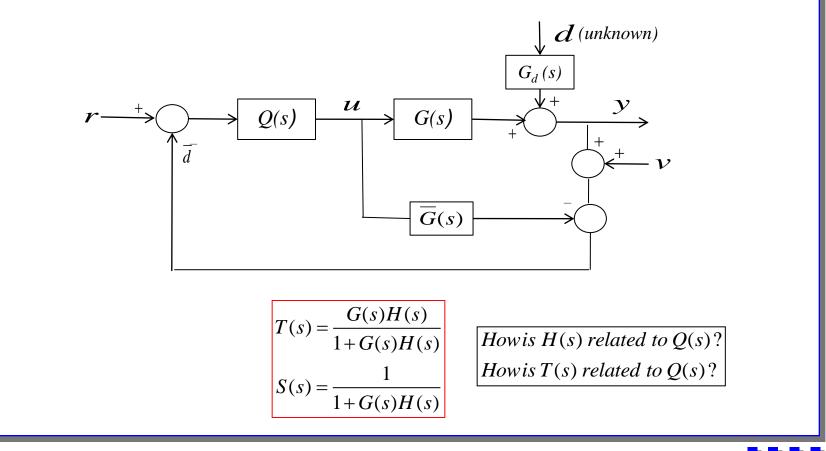
3) G(s) is not known perfectly,4) Disturbances are never known exactly





Internal Model Control (IMC)-1

- Widely used in Process Control (unknown disturbances, inaccurate models, constraints)
- A convenient theoretical framework for PID tuning rules, Smith predictor, non-minimum phase behavior,...



Internal Model Control (IMC)-2

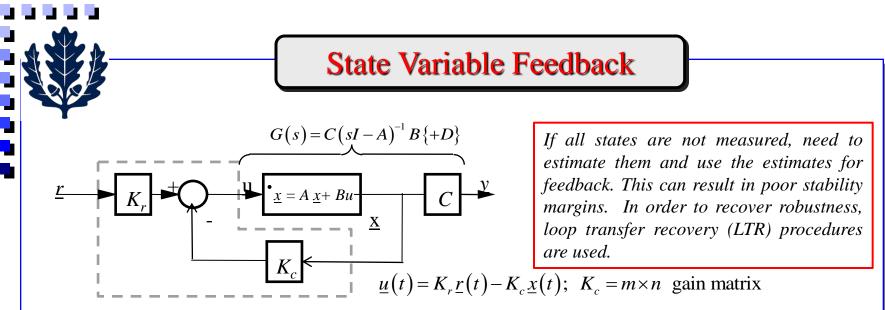
$$\begin{aligned} \operatorname{Re} \, call \, y(s) &= T(s)[r(s) - v(s)] + G_d(s)S(s)d(s) \\ Here \, y(s) &= \frac{G(s)Q(s)}{1 + Q(s)[G(s) - \overline{G}(s)]}[r(s) - v(s)] + \frac{[1 - \overline{G}(s)Q(s)]G_d(s)}{1 + Q(s)[G(s) - \overline{G}(s)]}d(s) \\ T(s) &= \frac{G(s)Q(s)}{1 + Q(s)[G(s) - \overline{G}(s)]} \Rightarrow Q(s) = \frac{H(s)}{1 + \overline{G}(s)H(s)} \text{ or } H(s) = \frac{Q(s)}{1 - \overline{G}(s)Q(s)} \\ When \, \overline{G}(s) &= G(s), T(s) = \overline{G}(s)Q(s) \text{ and } S(s) = 1 - \overline{G}(s)Q(s) \Rightarrow Q(s) = H(s)S(s) \\ \Rightarrow Both \, T(s) \text{ and } S(s) \text{ are linear in } Q(s) \\ \Rightarrow y(s) &= \overline{G}(s)Q(s)[r(s) - v(s)] + G_d(s)[1 - \overline{G}(s)Q(s)]d(s) = \overline{G}(s)Q(s)[r(s) - v(s)] + G_d(s)S(s)d(s) \end{aligned}$$

$$\overline{d}(s) = [G(s) - \overline{G}(s)]u(s) + v(s) + G_d(s)d(s)$$

if $G(s) = \overline{G}(s) \& d = v = 0, \overline{d} = 0$
 $\Rightarrow open - loop$
 $\overline{d} \neq 0 \Rightarrow filter this signal$

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Design objective is to have <u>y(t)</u>→ <u>r(t)</u>, especially when <u>r(t)</u> = step input, and to have desirable closed-loop stability/transient response. CL system dynamics are:

 $\underline{\dot{x}}(t) = (A - BK_c) \underline{x}(t) + BK_r \underline{r}(t)$ $\underline{y}(t) = C \underline{x}(t) + \{D\underline{u}(t)\}$

• Selection of feedback gains K_c so that eigenvalues of $A - BK_c$ are in suitable locations in the left hand *s*-plane and satisfy/optimize certain criteria

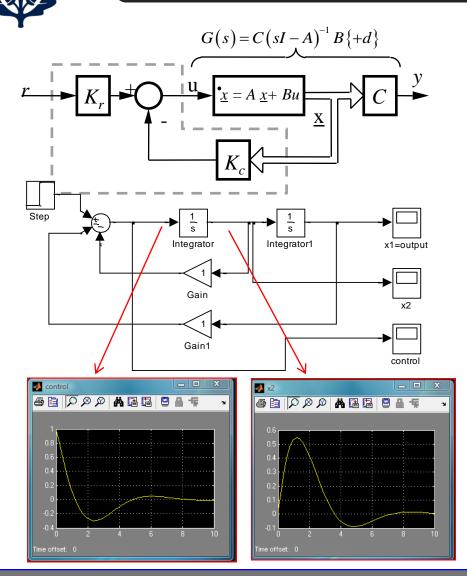
 \Rightarrow pole placement (eigen structure assignment), *LQR*, LQG, *H*₂, *H*_{∞}, μ -synthesis, *l*₁,...

• Crossover frequency and phase margin are evaluated by examining the Bode plot of

Loop gain =
$$K_c (sI - A)^{-1} B \Big|_{s = j\omega}$$

Continuous controllers require continuous (i.e., analog) feedback of y(t) and/or $\underline{x}(t)$ and implementation using analog components (e.g., circuits, op-amps, analog chips).

State Variable Feedback: SISO Example



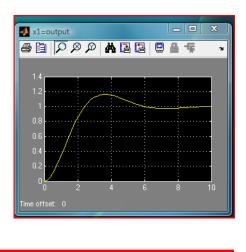
$$G(s) = \frac{1}{s^2}$$

$$A = \begin{bmatrix} 0 & 1 \\ 0 & 0 \end{bmatrix}; B = \begin{bmatrix} 0 \\ 1 \end{bmatrix}; C = \begin{bmatrix} 0 & 1 \end{bmatrix}$$

$$K_r = 1; K_c = \begin{bmatrix} 1 & 1 \end{bmatrix}$$

 $u(t) = K_r r(t) - K_c \underline{x}(t); K_c = 1 \times n$ gain matrix

1



loop gain,
$$K_c (sI - A)^{-1}B = \frac{s+1}{s^2}$$

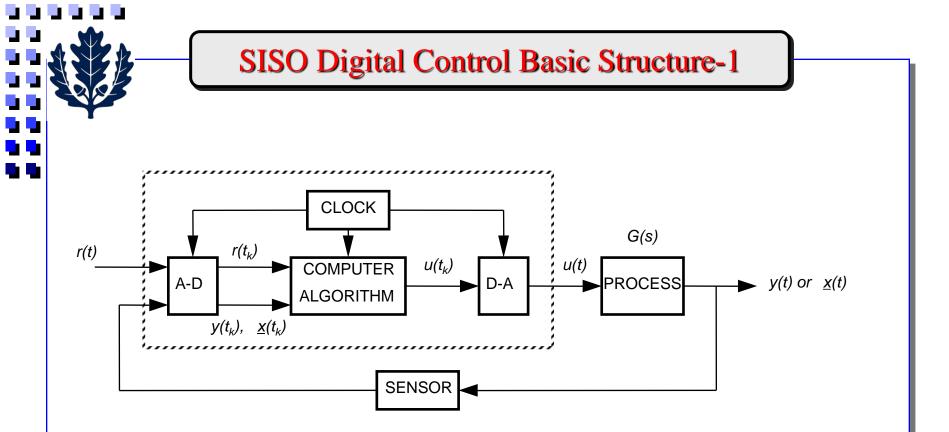
Gain M arg in, $\gamma_m = \infty db$
Phase M arg in, $\phi_m = 51.8^0$



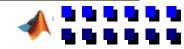
Classification of Controller Design Techniques

- Linear Time-invariant Systems
 - 1. Loop gain shaping
 - Classical designs (lead, lag, lead-lag, PID, feedforward-feedback,)
 - Loop-shaping to minimize sensitivity
 - 2. Closed-loop transfer function shaping (T(s), S(s), H(s)S(s))
 - Internal Model Control (IMC)
 - Minimize Mixed Weighted Sensitivity (H_{∞} optimal control)
 - 3. State variable feedback controllers
 - Pole placement, LQR, LQG, H_2 , H_{∞} , μ -synthesis, l_1 robust control,...
 - 4. Numerical optimization-based design
 - Linear Matrix Inequalities (LMI), Model Predictive Control (MPC)
- Non-linear Systems
 - Gain scheduling, MPC, Sliding mode control, Fuzzy control, Neural control





We are now dealing *not* with continuous signals in the controller, but with samples of these signals. Usually $t_k = kh$ where h is the sample time interval. The (real-time) clock maintains synchronism.





SISO Digital Control Basic Structure-2

- <u>Primary steps in computing u(t):</u>
 - 1. Wait for interrupt at time t_k .
 - (A-D) 2. Sample r(t) and y(t) to obtain $y(k) \triangleq$ value of y(t) at time $t = t_k$ $r(k) \triangleq$ value of r(t) at time $t = t_k$.

3. Compute u(k), u(k) = H[y(k), y(k-1), ..., r(k), ..., u(k-1), u(k-2), ...] This takes ε seconds.

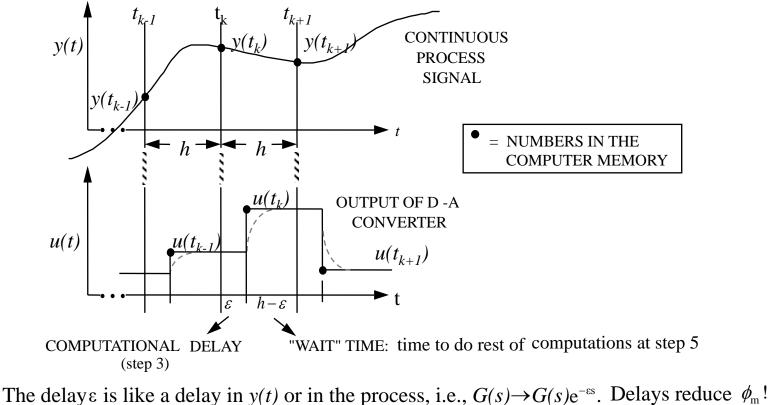
- (D-A) 4. Output u(k) through the D-to-A converter to give u(t). If the D-A is a hold circuit then u(k) = value of the control <u>over</u> the time interval $[t_k + \varepsilon, t_k + h + \varepsilon]$ where ε is the computational delay at step 3.
 - 5. Precompute any variables needed for the next cycle.
 - 6. Return to step 1 with k = k + 1.

All operations in steps 2-5 must be done in < h sec!



Relationship of Time Signals and Samples

There is often a computational delay at step 3, depending on the complexity of the computations performed.



It is important to minimize the delay at step 3 by arranging things so as to do the least amount of computations at step 3, while shifting the rest to step 5.



 $e(k) = r(k) - y(k) = \text{error at time } t_k$ $u(k) = -\alpha_1 u(k-1) - \alpha_2 u(k-2) - \dots - \alpha_m u(k-q) + \beta_0 e(k) + \beta_1 e(k-1) + \dots + \beta_m e(k-q)$

This is a <u>difference equation</u>, i.e., a relationship between a sequence of values. An alternate way of writing the algorithm is via a discrete transfer function. Notationally, $u(k) \rightarrow u(z)$, $u(k-i) \rightarrow z^{-i}u(z)$. Where z^{-1} is the unit shift, or unit delay, operator.

Referred to as an " q^{th} order compensator".

$$u(z) = H(z)e(z)$$

$$H(z) = \frac{\beta_0 + \beta_1 z^{-1} + \dots + \beta_q z^{-q}}{1 + \alpha_1 z^{-1} + \dots + \alpha_q z^{-q}}$$





(A Typical Algorithm For SISO H)-2

Implementation of Eq. (1.15):

1. Directly as shown at step 3. This would involve ~ (2q + 1) MADDS.

or

2. Compute $u(k) = WI + \beta_0 e(k)$ at step 3

where
$$WI = -\sum_{i=1}^{q} \alpha_i u(k-i) + \sum_{i=1}^{q} \beta_i e(k-i)$$

was computed at step 5 during the previous time step. Requires only *1* MADD!

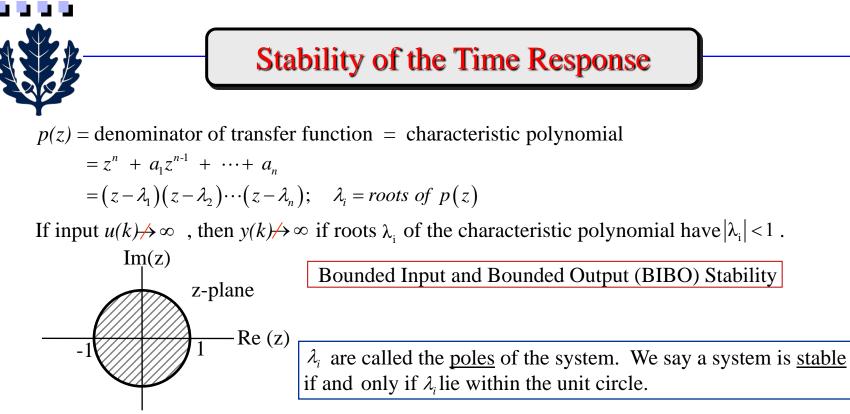
=> Clever organization of the algorithm can reduce ε .



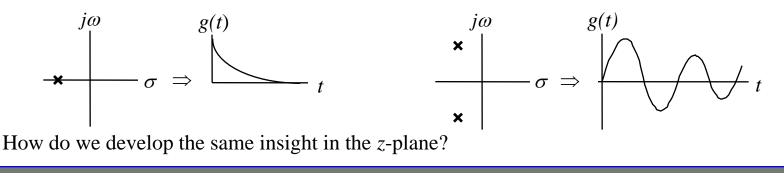
Diffs. in Digital vs. Analog Control Methods

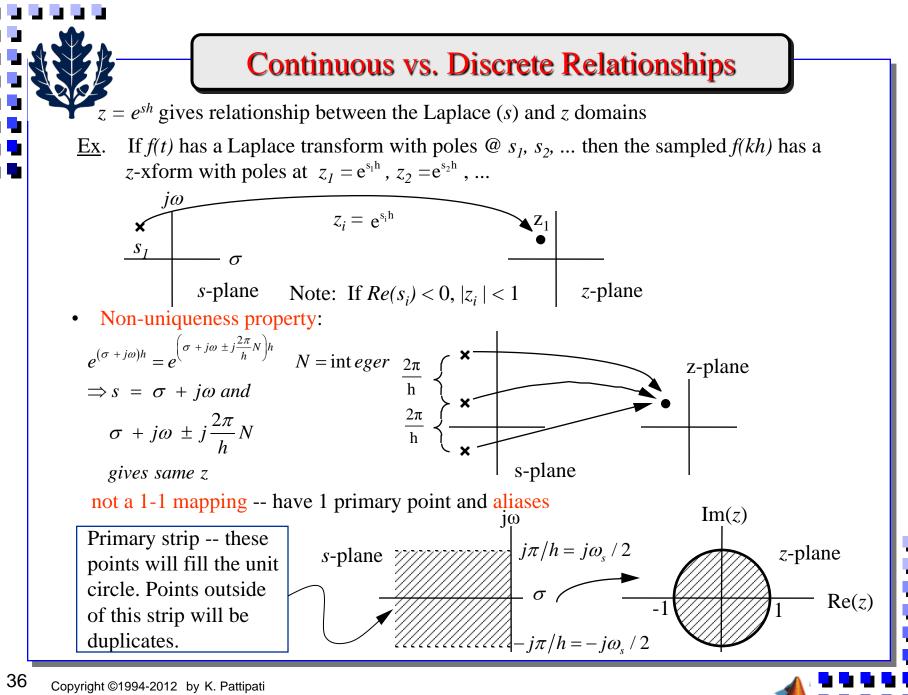
- Control design based on <u>samples</u> of y(t), r(t), x(t)
- Control input to system is piecewise constant over intervals of length *h* (assuming D -A is a hold circuit)
- Computational delays
- Controlling a <u>continuous</u> system, *G(s)*, using a <u>discrete</u> algorithm, *H(z)* => a mix of continuous and discrete elements constitutes the FB loop
- Most analysis will need to be performed using z-transforms and working in the z-plane
- Computer-aided design software becomes much more necessary for analysis, design, evaluation
- Effects of round-off error in computations due to finite word length
- Quantization error in A-D conversion (not a major issue with 16 and 24-bit A-D conversion)

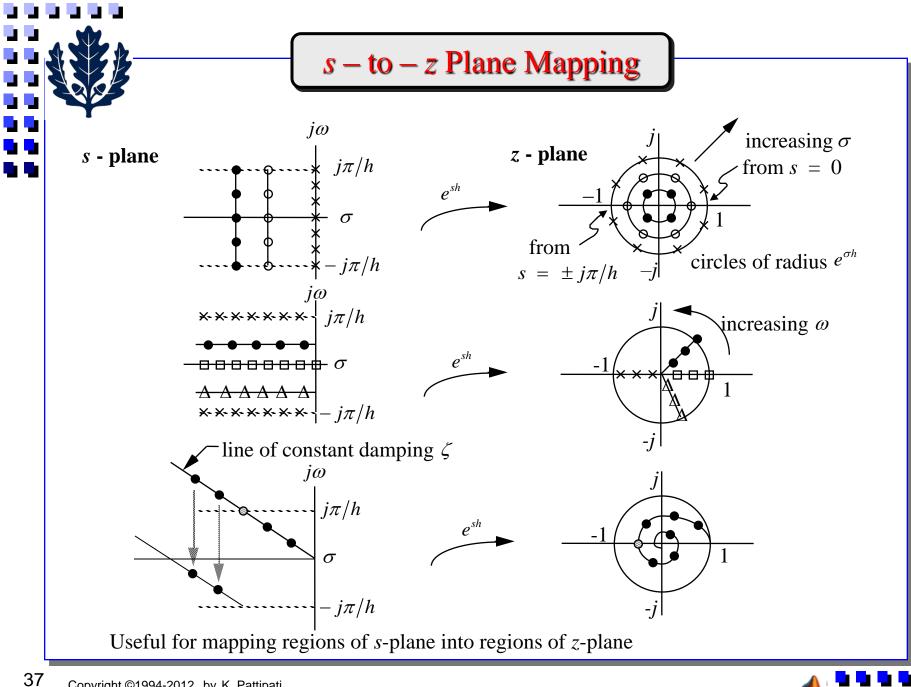


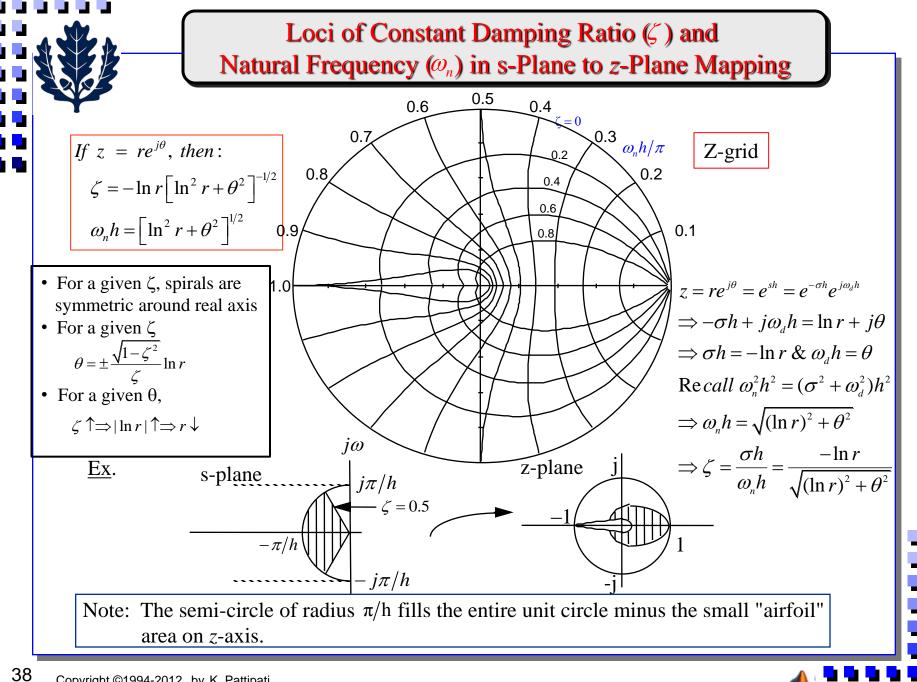


Usually the difference equation (1.19) is a discrete model of an underlying <u>continuous</u> process or continuous signal. We often have a good mental picture of the impulse response g(t) given the poles s_i of the continuous transfer function G(s).











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